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APPLICATION NO. FILING DATE		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/720,680		11/25/2003	Goran Gustafsson	P69331US0	3052	
136	7590	07/02/2004		EXAMINER		
		MAN PLLC	ANYA, IGWE U			
400 SEVE SUITE 600		EEI N.W.	ART UNIT	PAPER NUMBER		
WASHING	GTON, DO	20004	2825			
			DATE MAILED: 07/02/2004			

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applicat	tion No.	Applicant(s)				
Office Action Occasion			580	GUSTAFSSON ET	AL.			
	Office Action Summary	Examin	r	Art Unit	<del></del>			
		Igwe U.	-	2825				
Period f	The MAILING DATE of this communication Reply	tion appears on th	ne cover sheet with the c	correspondence add	dress			
THE - Extended - If th - If No - Fail Any	HORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNICA ensions of time may be available under the provisions of 3' r SIX (6) MONTHS from the mailing date of this communic e period for reply specified above is less than thirty (30) day of period for reply is specified above, the maximum statuto ure to reply within the set or extended period for reply will, reply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	TION. 7 CFR 1.136(a). In no election. 9 s, a reply within the strong period will apply and by statute, cause the ap	event, however, may a reply be ting atutory minimum of thirty (30) day will expire SIX (6) MONTHS from aplication to become ABANDONE	nely filed s will be considered timely the mailing date of this co D (35 U.S.C. § 133).				
Status								
1)⊠	Responsive to communication(s) filed o	n <u>25 November :</u>	<u>2003</u> .					
2a) <u></u> □	This action is <b>FINAL</b> . 2b)	☑ This action is	non-final.					
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposit	tion of Claims	•						
5)□ 6)⊠ 7)□ 8)□	Claim(s) 1-10 is/are pending in the apple 4a) Of the above claim(s) is/are version is/are allowed.  Claim(s) 1-10 is/are rejected.  Claim(s) is/are objected to.  Claim(s) are subject to restriction	vithdrawn from c						
Applicat	tion Papers							
10)⊠	The specification is objected to by the Extra The drawing(s) filed on <u>25 November 20</u> . Applicant may not request that any objection Replacement drawing sheet(s) including the The oath or declaration is objected to by	003 is/are: a)⊠ and to the drawing(s) are correction is required.	be held in abeyance. See ired if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CF	R 1.121(d).			
Priority	under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a) All b) Some * c) None of:  1. Certified copies of the priority documents have been received.  2. Certified copies of the priority documents have been received in Application No  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  * See the attached detailed Office action for a list of the certified copies not received.								
Attachmer								
	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-	948)	4) Interview Summary Paper No(s)/Mail Da	(PTO-413) ite.				
3) 🛛 Infor	mation Disclosure Statement(s) (PTO-1449 or PTC er No(s)/Mail Date <u>4/15/04</u> .	0-13) )/SB/08)	5) Notice of Informal P 6) Other:		-152)			

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## **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1 4, and 6 9 are rejected under 35 U.S.C. 102(e) as being anticipated by Tanaka (US Patent 6600225).
- 3. Tanaka teaches a method for forming interlayer connections in a layered electronic device (fig. 1C), particularly a thin-film device for storing or processing of data, wherein the device comprises electrical connections between circuitry located in two or more circuit layers separated by layers of electrically insulating material, wherein conducting material is applied as current paths on each circuit layer for connecting the circuitry located therein and joined with interlayer connections consisting of plugs or wires of highly electrically conducting material penetrating said interlayers of electrically insulating material, wherein the plugs or wires in the plane of said interlayers have a cross section with dimensions that are longer in one direction, with longitudinal dimension of representative magnitude Y and transversal dimension of representative magnitude X, such that Y>X (col. 5 lines 14 23);

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wherein the method is characterized by forming a plug or wire in one and the same step as used for applying the conducting material for a conducting path on an overlying circuit layer (col. 11 line 66 – col. 12 line 6);

characterized by providing said plug or wire for connecting current paths in the form of at least one narrow stripe electrode in one or more circuit layers, and in case of more than one, providing all stripe electrodes oriented in parallel, and by orienting said plug or wire such that its longitudinal dimension becomes parallel to the longitudinal direction of said at least one stripe electrode (fig. 1C element 5);

characterized by forming said plug or wire completely contained within a footprint of said at least one stripe electrode (fig. 1C element 5); and

characterized by forming the plug or wire with a ratio between the longitudinal and transversal dimensions Y resp. X such that Y/X > 2.5 (col. 5 lines 26 – 36).

## Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein

were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

- 6. Claims 5, and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tanaka (US Patent 6600225) in view Pinter (US Patent 5322816).
- 7. Tanaka teaches the features previously outlined, but lacks forming the plug or wire with the end sides along the short dimension tapering outwards towards the overlying circuit layer.
- 8. However, Pinter teaches forming the plug or wire with the end sides along the short dimension tapering outwards towards the overlying circuit layer (fig. 1D) to provide excellent step coverage for high conduction and reliability (col. 5 lines 14 16).
- 9. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the teachings of Pinter into the Tanaka reference to provide low resistance path for power or signal between interlayers.
- 10. Prior art considered, but not used in the rejection include Otsuka et al. (US Patent 6373136), Chidambarroa et al. (US Patent 6417572), and Inada et al. (JP 405160445).
- 11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Igwe U. Anya whose telephone number is (571) 272-1887. The examiner can normally be reached on M F 8:30am 5:00pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on (571) 272-1907. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Igwe U. Anya Examiner Art Unit 2825

IA

June 23, 2004

MATTHEW SMITH
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800